

## Super Low Distortion Multilayer Ceramic Capacitors

## UMK212SD103KD-T



## ■ Features

- Item Summary  
0.01  $\mu\text{F} \pm 10\%$ , 50V,  
Standard, 0805/2012 (EIA/JIS)
- Lifecycle Stage  
Mass Production
- Standard packaging quantity (minimum)  
Taping paper 4000pcs

## ■ Products characteristics table

Capacitance	0.01 $\mu\text{F} \pm 10\%$
Case Size (EIA/JIS)	0805/2012
Rated Voltage	50 V
$\tan \delta$ (max)	0.1 %
Operating Temp. Range (EIA)	-55 to +125 °C
Operating Temp. Range (JIS)	-55 to +125 °C
High Temperature Loading (% Rated Voltage)	200 %
Insulation Resistance (min)	10 G $\Omega$
RoHS2 Compliance (10 subst.)	Yes
REACH Compliance (173 subst.)	Yes
Halogen Free	Yes
Soldering	Reflow

## ■ External Dimensions

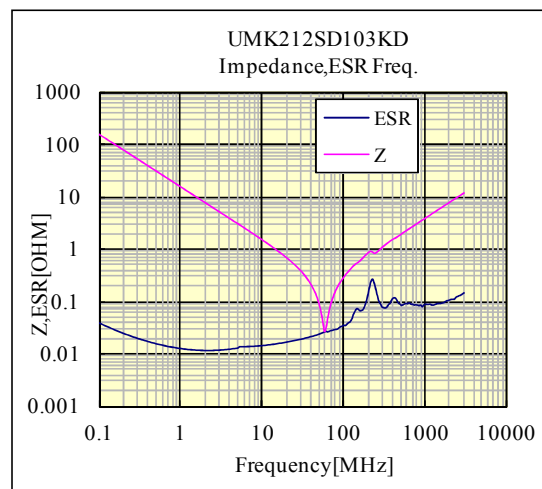
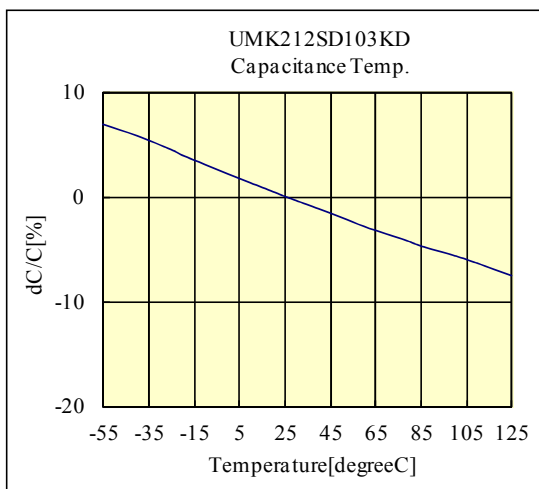
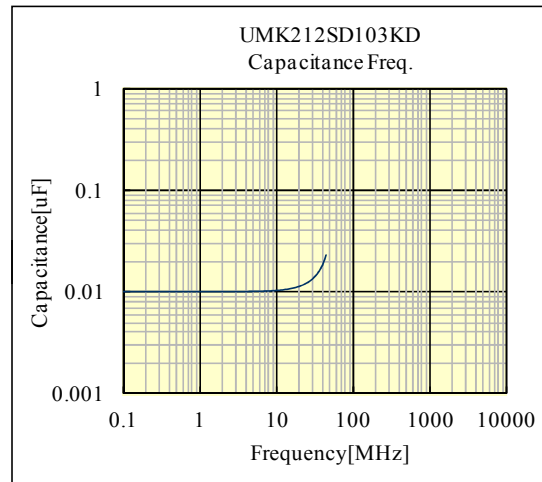
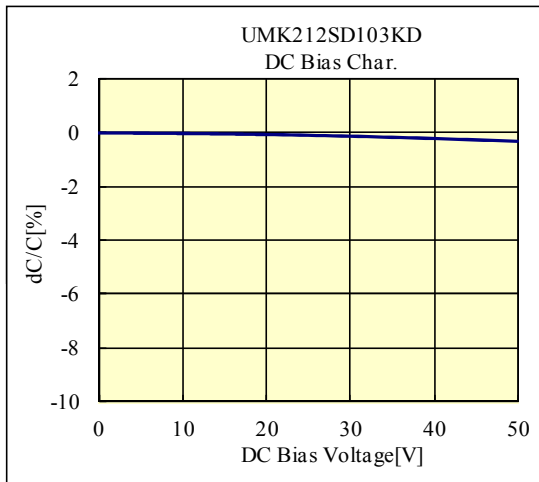
Dimension L	2.0 $\pm 0.10$ mm
Dimension W	1.25 $\pm 0.10$ mm
Dimension T	0.85 $\pm 0.10$ mm
Dimension e	0.50 $\pm 0.25$ mm

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The data is reference only. Electrical characteristics vary depending on environment or measurement condition.  
 TAIYO YUDEN reserves the right to make change to the Date at any time without notice.  
 Before making final selection, please check product specification.

## ○UMK212SD103KD

**50V 2012 0.01 $\mu$ F +/- 10% t=0.95mmMAX**  
**50V 0805 0.01 $\mu$ F +/- 10% t=0.037inchMAX**



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